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TECHNOLOGY CENTER 2800

2/13/03  
Smith

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takayoshi WATANABE, et al.

Application No.: 09/462,796

Filing Date: January 13, 2000

Title: SEMICONDUCTOR DEVICE, MOUNTING STRUCTURE  
THEREOF AND METHOD OF FABRICATION THEREOF

Art Unit: 2811

Examiner: Nitin Parekh

AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

January 27, 2003

Sir:

In response to the Office Action dated September 26, 2002, please amend  
the application as follows:

IN THE CLAIMS

Please cancel claims 1-33 without prejudice or disclaimer and add new  
claims 34-53 as follows:

C<sup>1</sup>  
34. A method of producing a semiconductor device comprising the steps of:  
forming a plurality of pyramidal bump electrodes of the semiconductor  
device; and